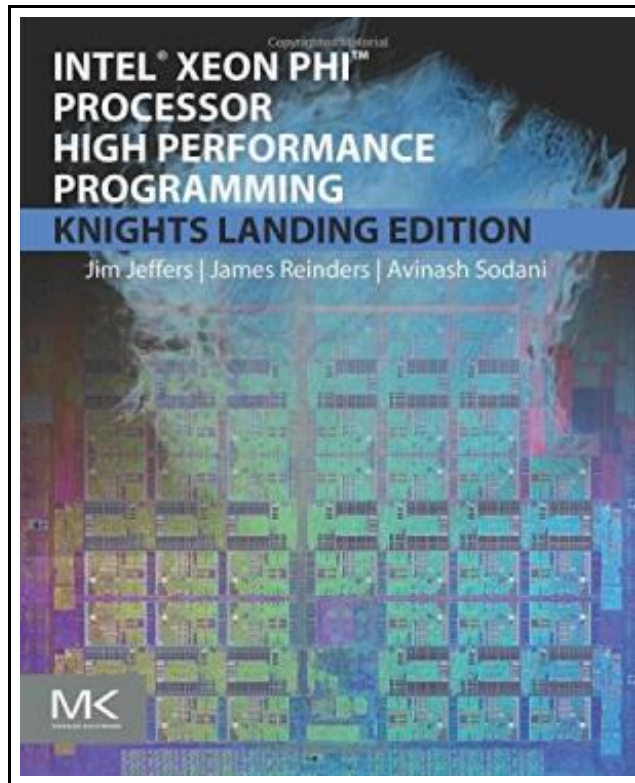# Intel Xeon Phi Processor High Performance Programming: Knights Landing Edition (Paperback)

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ELSEVIER SCIENCE TECHNOLOGY, United States, 2016. Paperback. Book Condition: New. 2nd Revised edition. 235 x 191 mm. Language: English . Brand New Book. This book is an all-in-one source of information for programming the Second-Generation Intel Xeon Phi product family also called Knights Landing. The authors provide detailed and timely Knights Landingspecific details, programming advice, and real-world examples. The authors distill their years of Xeon Phi programming experience coupled with insights from many expert customers - Intel Field Engineers, Application Engineers, and Technical Consulting Engineers - to create this authoritative book on the essentials of programming for Intel Xeon Phi products. Intel Xeon PhiT Processor High-Performance Programming is useful even before you ever program a system with an Intel Xeon Phi processor. To help ensure that your applications run at maximum efficiency, the authors emphasize key techniques for programming any modern parallel computing system whether based on Intel Xeon processors, Intel Xeon Phi processors, or other high-performance microprocessors. Applying these techniques will generally increase your program performance on any system and prepare you better for Intel Xeon Phi processors. * A practical guide to the essentials for programming Intel Xeon Phi processors* Definitive coverage of the Knights Landing architecture* Presents best practices for portable, high-performance computing and a familiar and proven threads and vectors programming model* Includes real world code examples that highlight usages of the unique aspects of this new highly parallel and high-performance computational product* Covers use of MCDRAM, AVX-512, Intel Omni-Path fabric, many-cores (up to 72), and many threads (4 per core)* Covers software developer tools, libraries and programming models* Covers using Knights Landing as a processor and a coprocessor.

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